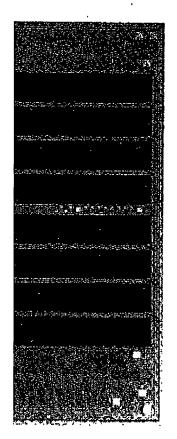
EXHIBIT A

Excerpts from Streetman, Ben G., Solid State Electronic Device, Prentice-Hall, Inc., 1980, page 67.



cuit chip employs charge-coupled This is achieved with 16 memories of tip (0.27 cm²). An additional 4 k-bit als, and peripheral input-output and modern integrated circuit technology which cannot be understood without evices. The purpose of this book is to er understand and use the solid state esy of Yexas instruments, inc.)

SOLID STATE ELECTRONIC DEVICES

second edition

BEN G. STREETMAN

Department of Electrical Engineering The University of Texas at Austin

11 doglot = 0 reading the of the second of t

Cowest consist consist and the North Consist of the North Consist of the Consist

PRENTICK-HALL, INC., Englewood Cliffs, New Jersey 07632

Library of Congress Cataloging in Publication Data

STREETMAN, BEN G Bolid state electronic dayloss.

Bibliography: p.
Includes Index.
1. Samiconductors. I. Title.
TK7371,85,877 1979 621,3815'2
ISBN 0-13-822171-5

79-16994

Editorial/production supervision and design by Virginia Rubens Cover design by Edsal Enterprises Manufacturing buyer: Gordon Osbourne Drawings by Scientific Illustrators, Champaign, Ill.

© 1980 by Prentice-Hall, Inc., Englewood Cliffs, N.J. 07632

All rights reserved. No part of this book may be reproduced in any form or by any means without permission in writing from the publisher.

Printed in the United States of America

10 9 8

PRENTICE-HALL INTERNATIONAL, INC., London
PRENTICE-HALL OF AUSTRALIA PTY. LIMITED, Sydney
PRENTICE-HALL OF CANADA, LTD., Toronto
PRENTICE-HALL OF INDIA PRIVATE LIMITED, New Delhi
PRENTICE-HALL OF JAPAN, INC., Tokyo
PRENTICE-HALL OF SOUTHEAST ASIA PTE. LTD., Singapore
WHITEHALL BOOKS LIMITED, Wellington, New Zealand

Preface

ΧV

1 Crystal Properties a

- 1.1 Semiconductor Mate
- 1.2 Crystal Lattices
 - 1.2.1 Periodic Struc
 - 1.2.2 Cubic Lattices
 - 1.2.3 Planes and Di
 - 1.2.4 The Diamond
- 1.3 Growth of Semicond
 - 1.3.1 Growth from t
 - 1.3.2 Zone Refining
 - 1.3.3 Liquid-Phase 1
 1.3.4 Vapor-Phase 1
 - 1.3.5 Molecular Bea
 - 1.3.6 Lattice Match.
- 2 Atoms and Electron
 - 2.1 Introduction to Physi
 - 2.2 Experimental Observa 2.2.1 The Photoelect 2.2.2 Atomic Spectra

CARRIERS IN SEMICONDUCTORS

Chapty

ken away from its position in the bondling to move about in the lattice, a conduction bond (hole) is left behind. The one band gap energy E_{θ} . This model helps in m of EHP creation, but the energy band surposes of quantitative calculation. One bond" model is that the free electron and in the lattice. Actually, the positions a spread out over several lattice spacing a mechanically by probability distributed.

are created in pairs, the conduction is per cm²) is equal to the concents les per cm²). Each of these intrinsic coned to as n. Thus for intrinsic material.

$$= p = n_i \tag{38}$$

ertain concentration of electron—hole parties concentration is maintained, there is same rate at which they are general electron in the conduction band makes an empty state (hole) in the valence band lenote the generation rate of EHP's at tion rate as r_i, equilibrium requires that

$$r_i = g_i$$
 (3)

$$_{0}p_{0}=\alpha _{r}n_{i}^{2}=g_{i}$$

n takes place. We shall discuss the calculature in Section 3.3.3; recombination till

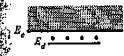
arriers generated thermally, it is possible s by purposely introducing impurities oping, is the most common technique

3.2 CHARGE CARRIERS IN SEMICONDUCTORS

the conductivity of semiconductors. By doping, a crystal can be refuse that it has a predominance of either electrons or holes. Thus there is detypes of doped semiconductors, n-type (mostly electrons) and p-type holes). When a crystal is doped such that the equilibrium carrier regations n_0 and p_0 are different from the intrinsic carrier concentration

daterial is said to be extrusic.

or impurities or lattice defects are introduced into an otherwise dystal, additional levels are created in the energy band structure, within the band gap. For example, an impurity from column V of iddic table (P, As, and Sb) introduces an energy level very near the lection band in Ge or Si. This level is filled with electrons at 0°K, and little thermal energy is required to excite these electrons to the conductand (Fig. 3-11). Thus at about 50-100°K virtually all of the electrons impurity level are "donated" to the conduction band. Such an impurity scalled a donor level, and the column V impurities in Ge or Si are called impurities. From Fig. 3-11 we note that material doped with donor ties can have a considerable concentration of electrons in the conductand, even when the temperature is too low for the intrinsic EHP mentitation to be appreciable. Thus semiconductors doped with a signif-









T = U - K

Rigure 3-11. Donation of electrons from a donor level to the conduc-

whiter of donor atoms will have $n_0 \gg (n_0, p_0)$ at room temperature.

oms from column III (B, Al, Ga, and In) introduce impurity levels of Si near the valence band. These levels are empty of electrons at ig. 3-12). At low temperatures, enough thermal energy is available like electr ns from the valence band into the impurity level, leaving holes in the valence band. Since this type of impurity level "accepts"